

Fig. 1

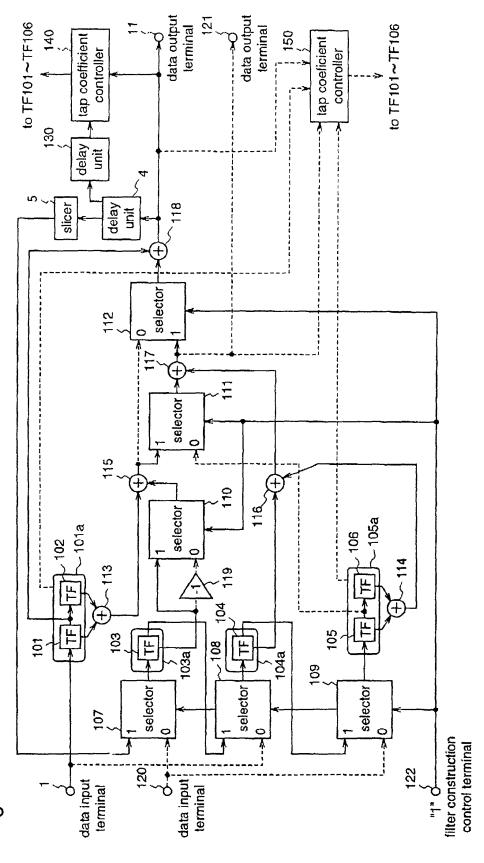
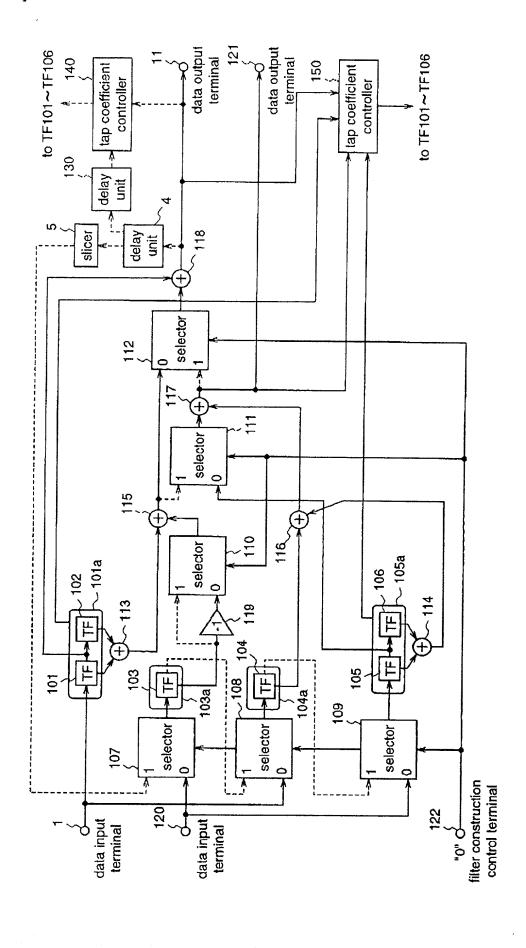


Fig.2





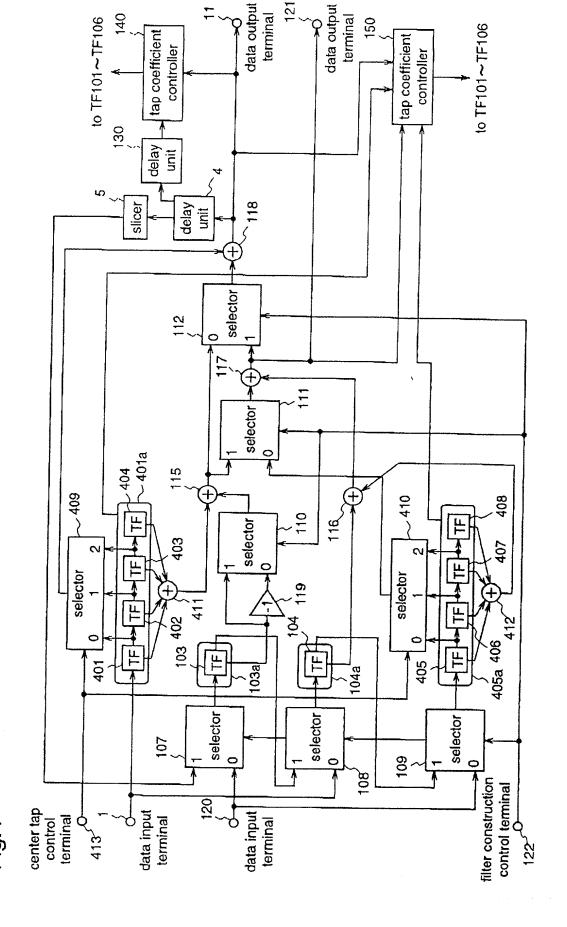
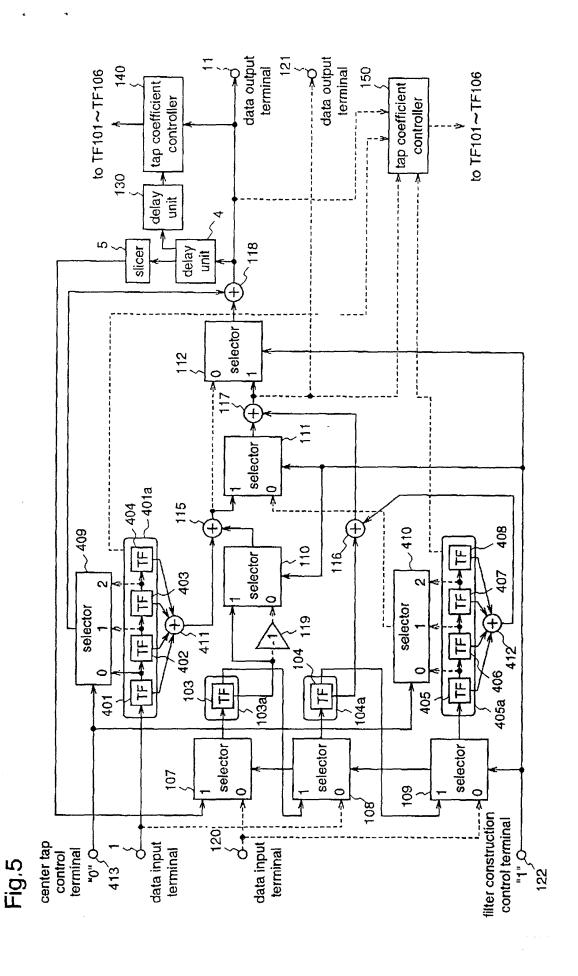


Fig.4



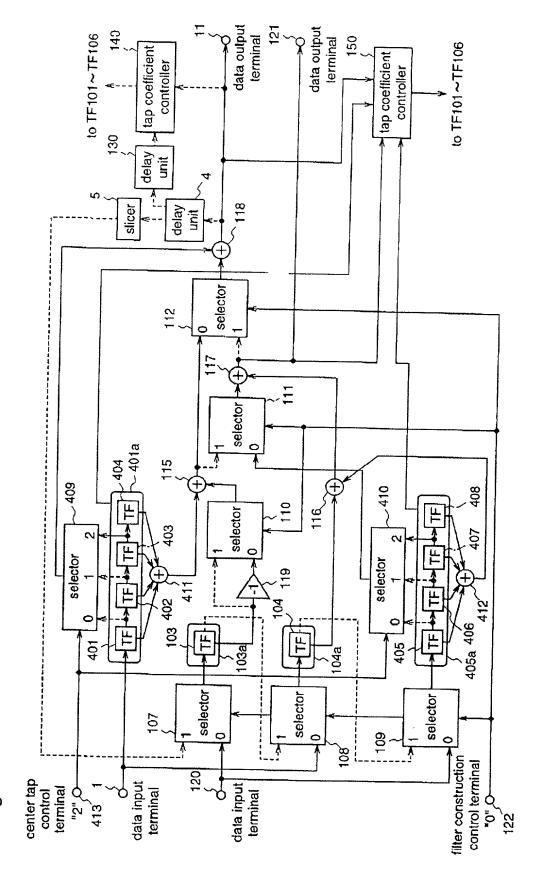


Fig.6

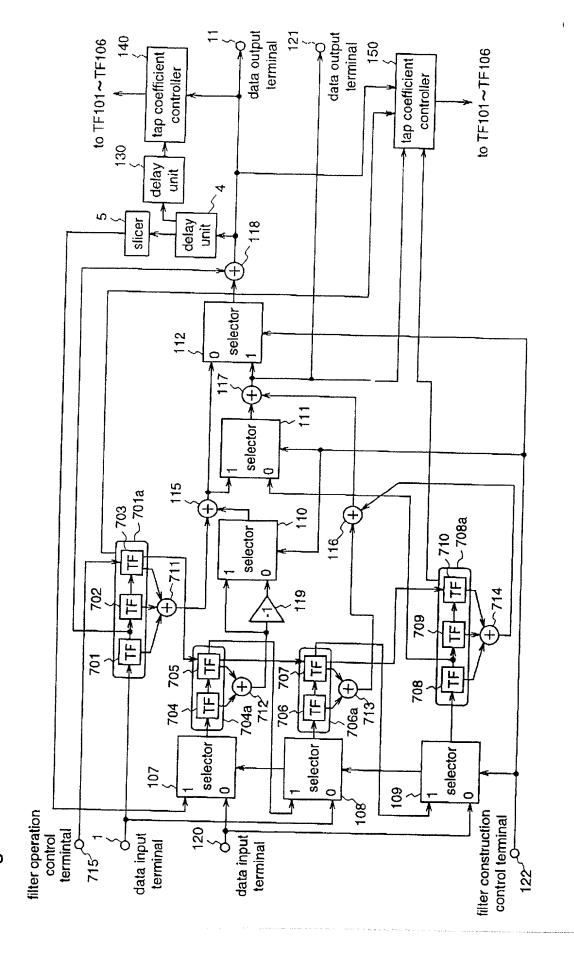


Fig.7

delay slicer E E selector selector 115 = (+ 110 selector 702 6 709 701 705 704a 🕀 707 708 704 902 706a < 713 selector selector selector 107 109 108 filter operation filter construction control terminal 120 control terminal data input terminal ٩ <u>-</u> data input P terminal Fig.8 Q o 715 ,22 ,22

data output terminal

150

tap coefficient

controller

to TF101~TF106

data output

terminal

to TF101~TF106

140

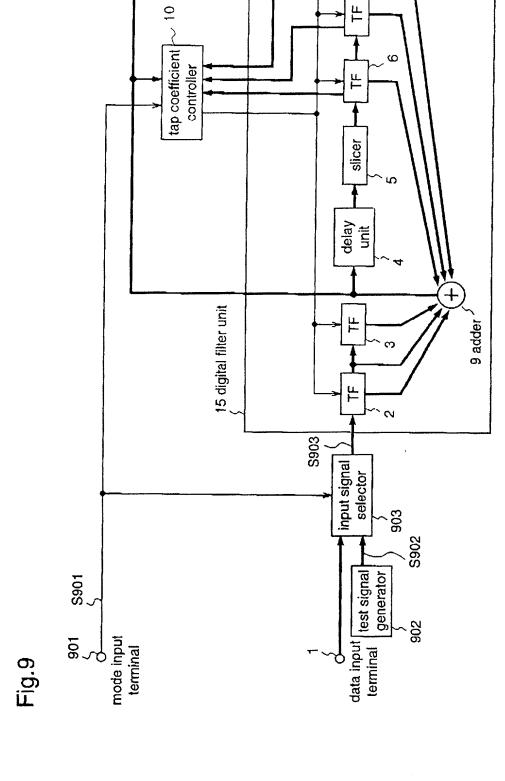
130

tap coefficient

delay

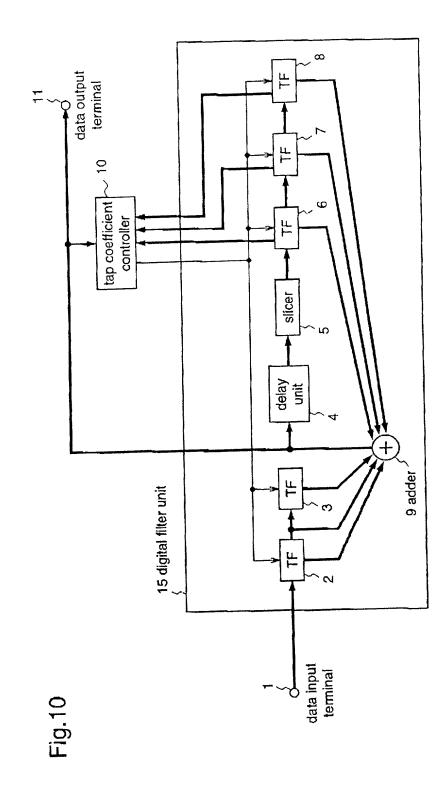
controller

121



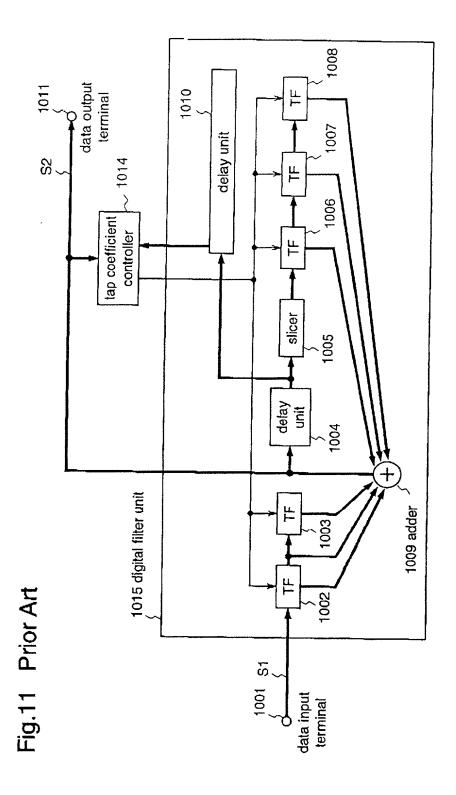
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11 { data output terminal



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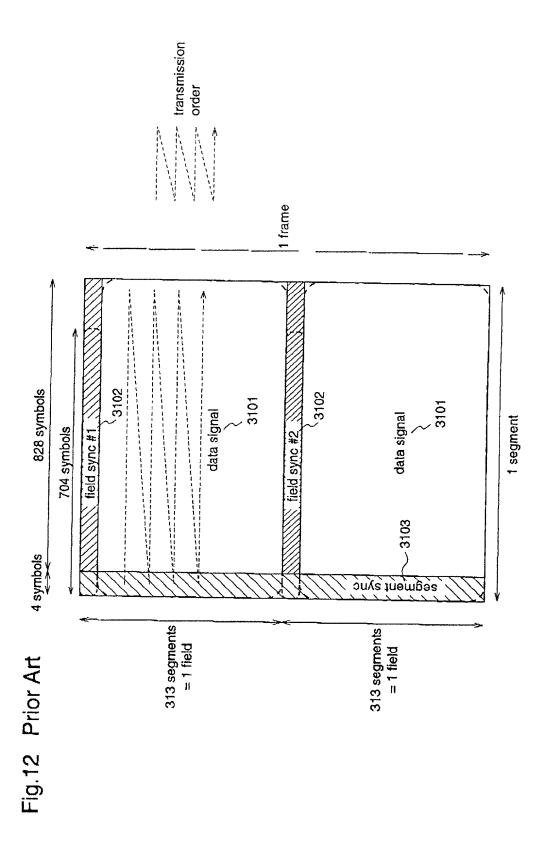


Fig.13 Prior Art

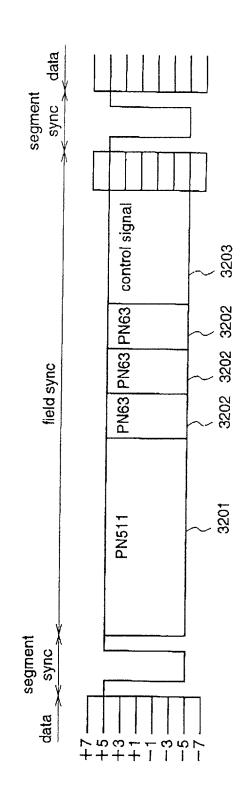


Fig.14 Prior Art

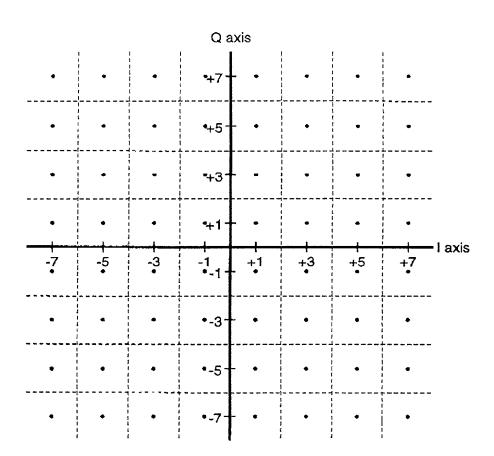


Fig.15 Prior Art

